



Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	09/396,486
				Filing Date	Sept. 14, 1999
				First Named Inventor	Joseph A. Urbano
				Group Art Unit	3737
				Examiner Name	Francis J. Jaworski
Sheet	1	of	1	Attorney Docket Number	1999P82189US

OTHER NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Barr, Michael. "How Programmable Logic Works." Netrino. 24 Mar. 2004. Netrino, LLC. 21 Sep. 2005 < http://www.netrino.com/Articles/ProgrammableLogic/ >	
		Barr, Michael. "Programmable Logic: What's It To Ya?." 1999. Embedded Systems Programming. 22 Sep. 2005 < http://www.embedded.com/1999/9906/9906sr.htm >	
		"EDA: Where Electronics Begins Addendum." techbites: simplifying technology. 2001. techBITES INTERActive. 22 Sep. 2005 < http://www.techbites.com/eda.html >	
		Jacobson, Neil G. "Internet Reconfigurable Logic Leveraging PLDs to Enhance Embedded System Functionality." TechOnline. 10 Dec 1999. Xilinx. 22 Sep. 2005 < http://www.techonline.com/community/ed_resource/feature_article/3454 >	
		"PLD." PLD Definition. 2004. Computer Desktop Encyclopedia. 22 Sep. 2005 < http://www.eweek.com/encyclopedia_term/0,2542,t=PLD&i=49377,00.asp >.	

Examiner Signature		Date Considered	3/3/06
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.